

CLAIMS:

1. An apparatus comprising:

a plurality of input terms; and

5 a summing module, to receive and sum the input terms using a hybrid Wallace tree architecture comprising a hyperpipelined series of Boolean function generator(s) and associated register(s) to implement one or more full-adders, half-adders, and associated registers necessary to sum the terms based, at least in part, on one or more attributes of the input terms.

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